PATENT	APPLICATION	SERIAL	NO.
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> PTO-1556 (5/87)

Electronic Filing System (EFS) Data Electronic Patent Application Submission USPTO Use Only

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SYSTEM AND METHOD FOR

PROTECTING AGAINST SHORT

Title of Invention:

CIRCUITS IN ELECTRIC POWER
DISTRIBUTION ARCHITECTURES

WITH TWO VOLTAGE LEVELS

First Named Inventor:

Carles Borrego Bel

Domestic/Foreign Application:

Domestic Application

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